Hardware/Software Codesign Lab 5

Student Name:

Student ID:

1. Follow the Lab 5 manual to finish Lab 5 and perform the following two demonstrations to your instructor:
   1. Program FPGA and download software application to the board to verify operations on hardware.
   2. Demonstrate step 4: Launch Debugger and debug
2. Copy and paste the following information to the end of this document:
3. Lab5.c
4. Answer the following questions:
   1. What is the prescale value of the private timer used in this lab?
   2. What is the minimum time interval and maximum time interval controlled by the dip-switch in this lab? Please show your calculation.
   3. List timer driver calling sequence.